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Digital Logic—Circuits and Boolean algebra

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1-Digital Logic Circuits—NUS UAV

Digital logic circuits handle data encoded in binary form, i.e. signals that have only two values, 0 and 1 . Binary logic dealing with “true” and “false” comes in handy to describe

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Description : This is an up-to-date treatment of the analysis and design of CMOS integrated digital logic circuits. The self-contained book covers all of the important digital circuit design styles found in modern CMOS chips, emphasizing solving design problems using the various logic styles available in CMOS.

Digital Logic Circuit Analysis and Design

4. Modular Combinational Logic. 5. Combinational Circuit Design with Programmable Logic Devices. 6. Introduction to Sequential Devices. 7. Modular Sequential Logic. 8. Analysis and Synthesis of Synchronous Sequential Circuits. 9. Simplification of Sequential Circuits. 10. Asynchronous Sequential Circuits. 11. Sequential Circuits with ...

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3 Digital Logic Circuits 1.2 Boolean Algebra and Logic Gates Boolean algebra (due to George Boole) is the mathematics of digital logic and is useful in dealing with binary system of numbers. Boolean algebra is used in the analysis and synthesis of logical expressions. Logical expressions are constructed using logical-variables and -operators.

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This is one of a series of videos where I talk about concepts relating to digital electronics. In this video I cover how to convert between Boolean algebra and logic gates.

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0. Introduction. 1. Number Systems and Codes. 2. Algebraic Methods for Analysis and Synthesis of Logic Circuits. 3. Simplification of Switching Functions. 4. Modular Combinational Logic. 5. Combinational Circuit Design with Programmable Logic Devices. 6. Introduction to Sequential Devices. 7. Modular Sequential Logic. 8. Analysis and Synthesis of Synchronous Sequential Circuits.

Digital Logic Circuit Analysis and Design by Victor P. Nelson

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DIGITAL LOGIC CIRCUITS

Introduction to Logic Circuits: Synthesis using AND, OR, and NOT gates: LECT04.pdf: Lecture 5: Introduction to Logic Circuits: Design Examples: LECT05.pdf: Lecture 6: Introduction to Logic Circuits: CAD Tools and VHDL: LECT06.pdf: Lecture 7: Optimized Implementation of Logic Functions: Karnaugh Maps and Minimum Sum-of-Product Forms: LECT07.pdf ...

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